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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN RE APPLICATION OF:	§	ATTY. DOCKET NO.:	BUR920030051US1
	§		
STANISLAV PETER BAJUK	§	EXAMINER:	TUYEN P. TO
	§		
SERIAL NO.: 10/604,071	§		
	§		
FILED: 25 JUNE 2003	§	ART UNIT:	2825
	§		
FOR: CODING OF FPGA AND	§		
STANDARD CELL LOGIC IN	§		
A TILING STRUCTURE	§		

AMENDMENT A UNDER 37 C.F.R. § 1.111

MAIL STOP NON-FEE AMENDMENT
COMMISSIONER FOR PATENTS
PO BOX 1450
ALEXANDRIA, VIRGINIA 22313-1450

Sir:

This Amendment is submitted in response to the Office Action dated December 29, 2005,
having a shortened statutory period set to expire March 29, 2006.

Please amend the present specification as indicated below.

Certificate of Transmission/Mailing

I hereby certify that this correspondence is being facsimile transmitted to the USPTO at 571.273.8300 or deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450 on the date shown below.

Typed Name: James E. Boice Date: February 2, 2006 Signature: 